

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6564179").PN.	USPAT	OR	OFF	2005/03/19 10:58
L2	1	1 and (virtual adj microcontroller)	USPAT	OR	ON	2005/03/19 11:25
L3	175	(four adj wire adj interface)	USPAT	OR	OFF	2005/03/19 11:25
L4	1	3 and ICE and microcontroller	USPAT	OR	OFF	2005/03/19 11:26
L5	10	3 and emulat\$5 and microcontroller	USPAT	OR	OFF	2005/03/19 11:28
L6	283	(four adj wire adj interface) or ("4" adj wire adj interface)	USPAT	OR	ON	2005/03/19 11:29
L7	0	6 and (in adj circuit adj emulator)	USPAT	OR	ON	2005/03/19 11:30
L8	28	6 and emulation	USPAT	OR	ON	2005/03/19 11:30
L9	11	8 and (system adj clock)	USPAT	OR	ON	2005/03/19 11:35
L10	1	JTAG and ICE and (four adj wire) and interface	USPAT	OR	ON	2005/03/19 11:37
L11	8091	(ICE).ti.	USPAT	OR	OFF	2005/03/19 11:37
L12	10	(In adj Circuit adj Emulator).ti.	USPAT	OR	OFF	2005/03/19 11:40
L13	95	(In adj Circuit adj Emulator)	USPAT	OR	OFF	2005/03/19 11:40
L14	1	13 and (four adj wire)	USPAT	OR	OFF	2005/03/19 11:40